

### **Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of the Claims**

1. (Currently Amended) A circuit arrangement having  
a first power supply unit,  
a rectifier supplying a DC voltage for said power supply unit,  
a mains terminal,  
a mains switch having a first switching contact, arranged between the mains  
terminal and the rectifier, or arranged for turning off a supply or control voltage of a driver  
stage of the first power supply unit, and  
a switching element with a control terminal, to which a control voltage is coupled,  
the switching element ~~bridging a~~ being coupled in parallel with said first switching contact  
of the mains switch for providing a current path bridging said first switching contact,  
a controller, which is supplied with an operating voltage by the first power supply  
unit, and which is coupled to said control terminal for control of the switching element, and  
~~wherein a load is coupled to a said control terminal of the switching element for~~  
~~turning~~ switching the load off substantially simultaneously when opening the switching  
element via the control voltage by means of the controller, and wherein the load is switched  
off and the switching element is opened in a delayed manner, when the circuit arrangement  
is switched off by means of the mains switch.
2. (Currently Amended) The circuit arrangement as claimed in claim 1, wherein the  
~~circuit arrangement has a controller, in particular is a microprocessor, which is supplied~~  
~~with an operating voltage by the power supply unit, and which is coupled to the control~~  
~~terminal of the switching element for control of the switching element.~~
3. (Currently Amended) The circuit arrangement as claimed in claim 2-1, wherein the  
switching element is switched on and off by the controller via a switch arranged between an  
output voltage of the power supply unit and the control terminal.

4. (Currently Amended) The circuit arrangement as claimed in claim 1, wherein the load is a fan, ~~in particular, which is switched off in a delayed manner simultaneously when the circuit arrangement is switched off by means of the mains switch, by virtue of the switching element being opened in a delayed manner.~~
5. (Previously Presented) The circuit arrangement as claimed in claim 1, wherein an inductance for a power factor correction is arranged between a mains terminal and the power supply unit, in particular upstream of a bridge rectifier.
6. (Currently Amended) The circuit arrangement as claimed in claim 1, comprising a second power supply unit, wherein ~~a~~the first power supply unit has a lower output power and ~~a~~the second power supply unit has a higher output power, the second power supply unit being a switch mode power supply unit, ~~in particular~~, and the circuit arrangement ~~has~~ having a standby mode in which the second power supply unit is turned off.
7. (Previously Presented) The circuit arrangement as claimed in claim 6, wherein the load is supplied with an operating voltage by the second power supply unit via a decoupling element in the normal mode and is supplied with an operating voltage by the first power supply unit via a switch in the standby mode.
8. (Currently Amended) The circuit arrangement as claimed in claim ~~1-6~~, ~~wherein the first switching contact of the mains switch is arranged between a mains terminal and a rectifier and~~ a second switching contact of the mains switch is used for turning off a supply or control voltage of a driver stage of the second power supply unit.
9. (Currently Amended) The circuit arrangement as claimed in claim ~~1-6~~, wherein ~~the first switching contact is used for turning off a supply or control voltage of a driver stage of the first power supply unit and~~ a second switching contact of the mains switch is used for turning off a supply or control voltage of a driver stage of the second power supply unit.
10. (Currently Amended) The circuit arrangement as claimed in claim 2, wherein the ~~controller~~ microprocessor comprises a timer program for opening the switching element

and for turning off the load.

11. (Previously Presented) The circuit arrangement as claimed in claim 1, comprising further a mains switch detection circuit coupled to the controller for sensing a switching off operation of the mains switch.

12. (Previously Presented) The circuit arrangement as claimed in claim 1, wherein the circuit arrangement is arranged in an image projection device, in particular a rear projection television set using a DLP (Digital Light Processing) unit, the image projection device having a das discharge lamp to be cooled which, after it has been switched off, has to be cooled for a defined time, i.e. approximately 1-2 minutes.